COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

			NG SEMICONDUCTOR DI	EVICES			
			, the specification of which				
(check one)	<u>X</u>	is attached h	nereto.				
		was filed on Application	Serial No.	as ·			
-			ed and understand the contents anded by any amendment referr				
	_	•	se information which is materi 7, Code of Federal Regulations				
any foreign appl	lication(s) any foreign	for patent or application f	enefits under Title 35, United inventor's certificate listed for patent or inventor's certific prity is claimed:	below and have also			
Prior Foreign App	olication(s))		Priority Claimed			
2003-144480)	JAPAN	22/05/2003	X_Yes No			
(Number)		(Country)	(Day/Month/Year Filed)				
				Yes No			
(Number)		(Country)	(Day/Month/Year Filed)				
				Yes No			
(Number)		(Country)	(Day/Month/Year Filed)	<u>.</u>			

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

•		-			
(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned) (Status-patented, pending, abandoned)			
(Appln. Serial No.)	(Filing Date)				
prosecute the patent applie and Trademark Office con Jr. (Reg. No. 28,149); K 28,562); Paul Devinsky (R	cation identified above nected therewith: Ra enneth L. Cage (Reg leg. No. 28,553); Edwa 39); Brian E. Ferguso	full power of substitution and revocation, to e and to transact all business in the U.S. Patent phael V. Lupo (Reg. No. 28,363); Jack Q. Lever J. No. 26,151); Stanislaus Aksman (Reg. No. ard E. Kubasiewicz (Reg. No. 30,020), Michael on (Reg. No. 36,801); Robert W. Zelnick (Reg. 37,136).			
Please address all correspondence	ondence and telephone	calls to:			
	Jack Q. Lever, Jr. McDERMOTT, WILL 600 Thirteenth Street, Washington, D.C. 20 (202) 756-8000	N.W.			
instructions from Ma Trademark Office regardinattorney and the undersign	teda Patent Office ng this application with the event of a	S. attorneys named herein to accept and follow as to any action to be taken in the Patent and thout direct communication between the U.S. change in the persons from whom instructions ill be so notified by the undersigned.			

I hereby declare that all statements made herein of my own knowledge are true and that

all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any

Inventor's signature <u>Latoshi</u> <u>Tamura</u> Date May 13, 2004

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patent issued thereon.

Full name of sole or first inventor Satoshi TAMURA

Full name of second inventor <u>Tetsuzo UEDA</u>									
Inventor's signa	ature <u>IEZo</u>	Veden	_ Date 1	May 13,	20041				
	Osaka, Japan		tizenship	Japan					
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^{*} City and State, or City and Country for foreign inventors

Docket No.: 60188-861

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Satoshi TAMURA, et al.

Confirmation Number:

Serial No.:

Group Art Unit:

Filed: May 21, 2004

Examiner:

For:

METHOD FOR FABRICATING SEMICONDUCTOR DEVICES

ASSOCIATE POWER OF ATTORNEY

Mail Stop Associate Power of Attorney Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The undersigned Principal Attorney of record hereby appoints the following Attorneys as his Associates with regard to the above-identified application: Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Richard E. Brown, Reg. No. 47,453; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Alex H. Chan, Reg. No. 52,713; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Ramyar M. Farid, Reg. No. 46,692; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Keith E. George, Reg. No. 34,111; Thomas A. Haag, Reg. No. 47,621; John A. Hankins, Reg. No. 32,029; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Michael A. Messina, Reg. No. 33,424; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Gene Z. Rubinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti, Reg. No. 39,048; Kelli N. Watson, Reg. No. 47,170; Cameron K. Weiffenbach, Reg. No. 44,488; Aaron Weisstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; William Young, Reg. No. 54,718; and Wei-Chen Chen and Tomoki Tanida, admitted under 37 CFR 10.9(b) all of

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